

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED
CENTRAL FAX CENTER

APR 03 2007

In re Applicant:	Shawn L. Lloyd et al.	§	Art Unit:	2833
Serial No.:	10/822,572	§	Examiner:	James R. Harvey
Filed:	April 12, 2004	§	Docket:	ITL.1113US P18785
For:	Making Electrical Connections Between a Circuit Board and an Integrated Circuit	§	Assignee:	Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

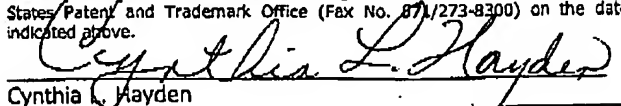
SUPPLEMENTAL REPLY TO FINAL REJECTION

Sir:

As a supplemental reply to the final rejection, please amend the above-referenced patent application as follows:

Date of Deposit: April 3, 2007

I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 877/273-8300) on the date indicated above.


Cynthia L. Hayden

TROP, PRUNER & HU, P.C.

RECEIVED
CENTRAL FAX CENTER

APR 03 2007

INTELLECTUAL PROPERTY LAW ATTORNEYS

1616 South Voss Road, Suite 750
Houston, Texas 77057-2631

Bus: (713) 468-8880

Fax: (713) 468-8883

Fax

To:	Examiner James R. Harvey	From:	Timothy N. Trop
Company:	US Patent Office	Date:	April 3, 2007
Fax:	(571) 273-8300	Pages:	7 (including cover sheet)
Your Re:	10/822,572	Our Re:	ITL.1113US

☐ Urgent ☐ For Review ☐ Please Comment ☐ Please Reply ☐ Confirm Receipt

MESSAGE:

● **Notice:** This information is intended to be for the use of the individual or entity named on this transmittal sheet. If you are not the intended recipient, be aware that any disclosure, copying, distribution, or use of the contents of this faxed information is prohibited. If you have received this facsimile in error, please notify the sender by telephone immediately so that arrangements can be made for the retrieval of the original document at no cost to you.